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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10062595	FILING DATE 01/31/2002	CLASS 072	SUBCLASS 456	GAU 3725	EXAMINER Jones
**APPLICANTS: Siegel Harry; Chiu Anthony;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged: Examiners's initials		01-C-055 (STMI01-01055)			
TITLE : System and method for aligning an integrated circuit die on an integrated circuit substrate					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner Primary Examiner	
ISSUE FEE Amount Due Date Paid		Total Claims Print Claim for O.G. DRAWING Sheets Drwg. Figs. Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner PREPARED FOR ISSUE	
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